## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Jefferson W. Hall IN RE APPLICATION OF:

Concurrently Herewith FILED:

DIFFERENTIAL TRANSISTOR AND METHOD THEREFOR FOR:

9/22/2003 Date:

## DISCLOSURE STATEMENT UNDER 37 C.F.R. 1.56

Honorable Assistant Commissioner for Patents Washington, D.C. 20231

SIR:

It is respectfully requested that the prior art listed on FORM PTO-1449 be considered in the examination of the subject application and made of record therein. A copy of the listed prior art is enclosed herewith.

No representation is made or intended that the listed prior art enclosed herewith is material to patentability of the subject patent application.

No representation is made or intended that a search has been made or that no better prior art than that listed is available.

Respectfully submitted,

Jefferson W. Hall

James J. Stipanuk

Req. #44,358

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Intellectual Property Dept. P.O. Box 62890 - A700 Phoenix, AZ 85062-2890

Customer #: 27255

ON Semiconductor

REFERENCE DESIGNATION U.S. PATENT I	DOCUMENTS							
STATEMENT (Use Several Sheets if Necessary)	CONCURRENTLY							
DISCLOSURE	FILING DATE	GROUP						
FOR APPLICANT'S INFORMATION	APPLICANT Jefferson W. Hall							
LIST OF PATENTS AND PUBLICATIONS	ONS00459							
Form PTO-1449	ATTY. DOCKET NO.	SERIAL NO.						

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## **FOREIGN PATENT DOCUMENTS**

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		OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)	
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EXAMINER DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.